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	Filing Date		2004-11-18
	First Named Inventor	ANTON ROZEN, et al.	
	Art Unit	8855	
	Examiner Name	HERNANDEZ, MANUEL J.	
	Attorney Docket Number	SC12938EI	

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	1	7110932	B2	2006-09-19	BERTHOLD et al.	
	2	7336092	B1	2008-02-26	KONIARIS et al.	
	3	7430673	B3	2008-09-30	KARDACH et al.	
	4	7464278	B2	2008-12-09	COHEN et al.	
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	1	SRINIVASAN, ET AL., IBM Research Report, RAMP: A Model for Reliability Aware MicroProcessor Design, IBM Research Division, Computer Science, December 29, 2003	<input type="checkbox"/>
	2	WALDSCHMIDT, ET AL., Reliability-Aware Power Management of Multi-Core Systems (MPSoCs), Dagstuhl Seminar Proceedings 06141, Dynamically Reconfigurable Architectures, pgs. 1-7	<input type="checkbox"/>
	3	J.H. STATHIS, Reliability limits for the gate insulator in CMOS technology, IBM J. Res. & Dev., Vol. 46, No. 2/3, March/May/2002	<input type="checkbox"/>
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